

# Hacking 2 048 RSA code with 8 100 qubits and a multimode memory with 2 hours storage time

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**Élie Gouzien** and Nicolas Sangouard

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Institut de Physique Théorique, CEA Saclay

# Quantum computing hardware

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Superconducting qubits, trapped atoms/ions, optics, spins...

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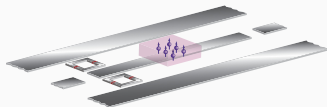
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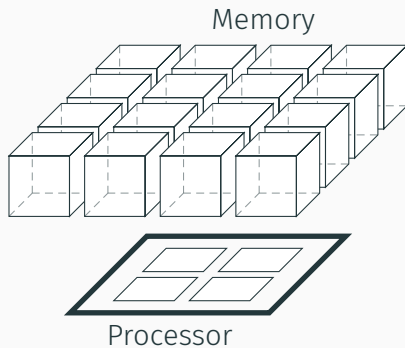
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### Superconducting processor + memory

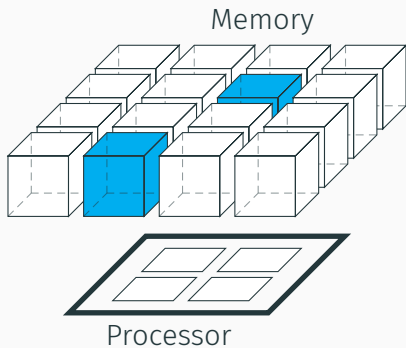


Cécile Grezes et al. "Towards a spin-ensemble quantum memory for superconducting qubits".  
*Comptes Rendus Physique* 17.7  
(2016), pp. 693–704

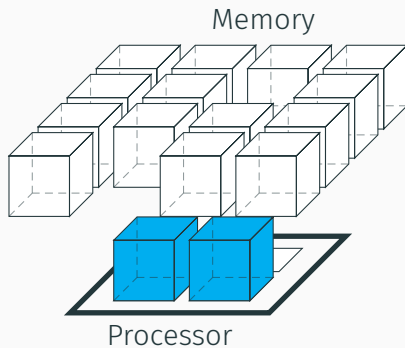
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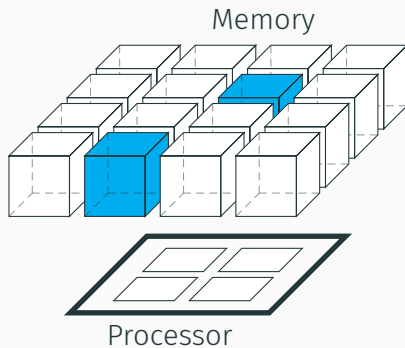


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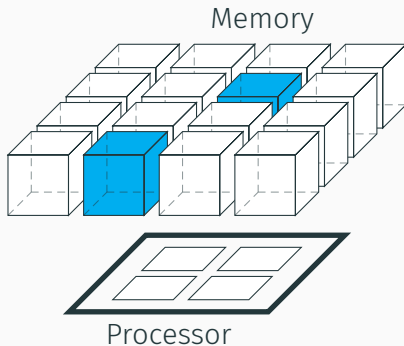




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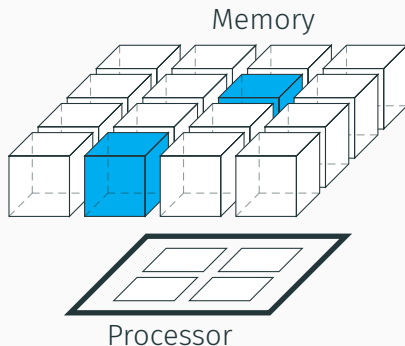
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## Potential benefits

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What is quantitatively the advantage brought by this architecture?

## Shor's algorithm (and variants)

- classically hard: no polynomial algorithm
- breaking RSA shows importance of quantum communications
- check the correct operation of the machine
- mostly sequential

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## Factorization with 2D superconducting processor

Craig Gidney and Martin Ekerå. "How to factor 2048 bit RSA integers in 8 hours using 20 million noisy qubits". 2019

# Factorization algorithm

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# Shor's algorithm

## Problem

$N = pq$ ,  $p$  and  $q$  prime numbers

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## Run on quantum hardware

1. prepare  $\frac{1}{\sqrt{2^{n_e}}} \sum_{x=0}^{2^{n_e}-1} |x\rangle$ ,  $n_e$  number of bits of the exponent
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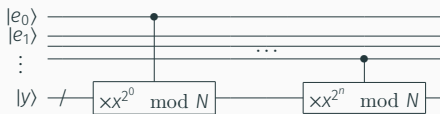
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# Standard implementation complexity

Exponentiation:  $|e\rangle |y\rangle \mapsto |e\rangle |y \times x^e \bmod N\rangle$

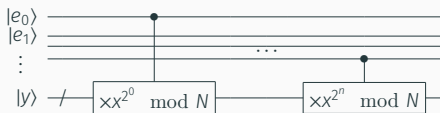
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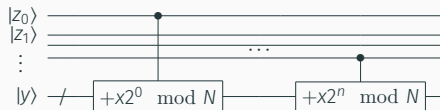
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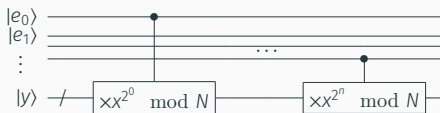




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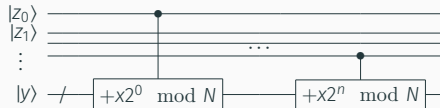
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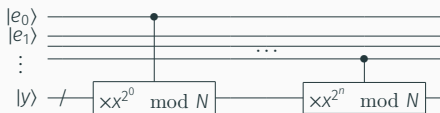
$O(n)$  with carry-ripple addition

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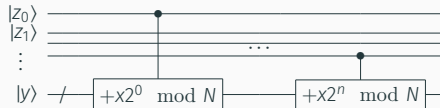
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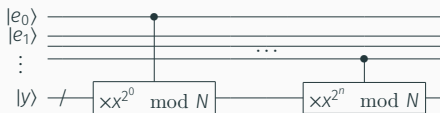
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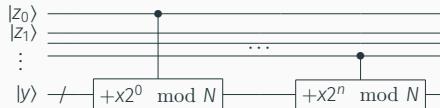
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**Complexity**

Modular exponentiation:  $O(n^3)$

## Advanced algorithm complexity

### Ekerå and Håstad's algorithm

Size of the exponent:  $1.5n$  instead of  $2n$  for Shor's algorithm

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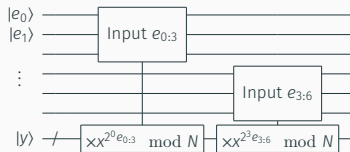
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## Windowed arithmetic circuits

Craig Gidney. "Windowed quantum arithmetic". 2019

Exponentiation example:

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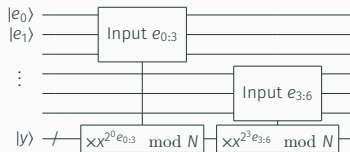
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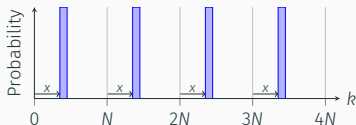
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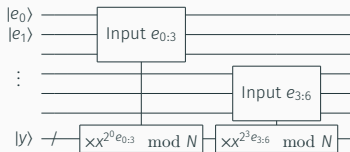
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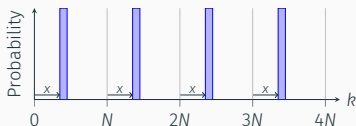
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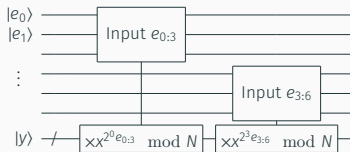
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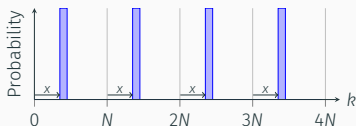
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$\Rightarrow O(n^3)$  but better pre-factors than basic algorithm

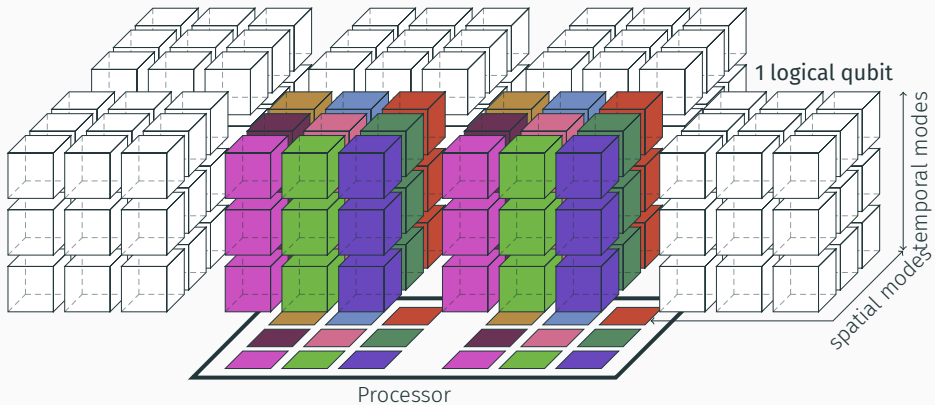


## 3D gauge color codes

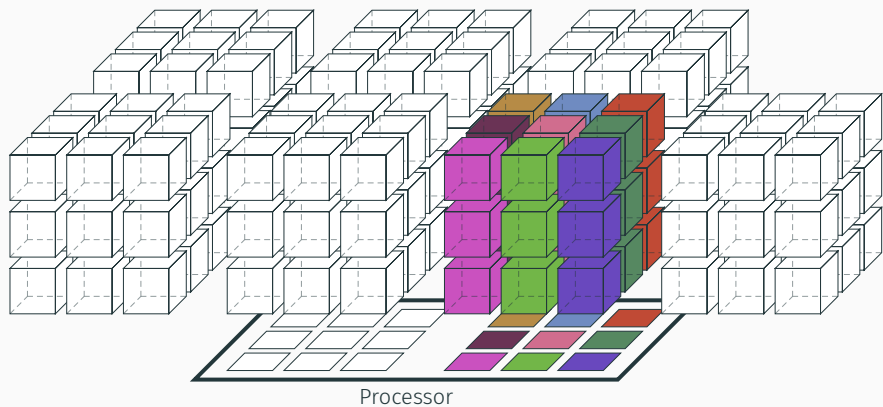
- $H$ , CNOT and  $T$  gates transversal
  - ⇒ universal set
  - ⇒ no distillation/gate teleportation
- 3D code, 2D processor
  - ⇒ 2D parallelism for stabilizers measurements
- single-shot error correction: no need to repeat stabilizers measurement
- Error threshold: 0.75 %

Héctor Bombín. "Gauge color codes: optimal transversal gates and gauge fixing in topological stabilizer codes". *New Journal of Physics* 17.8 (2015), p. 083002

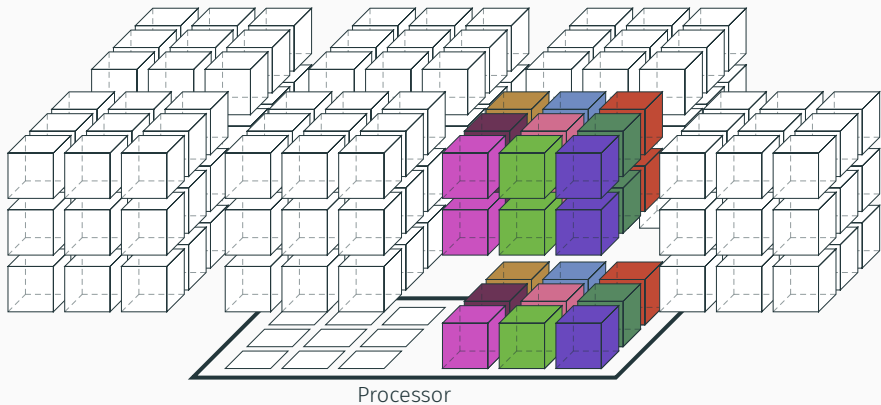
# How to measure syndromes



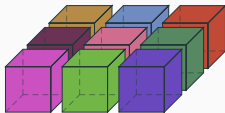
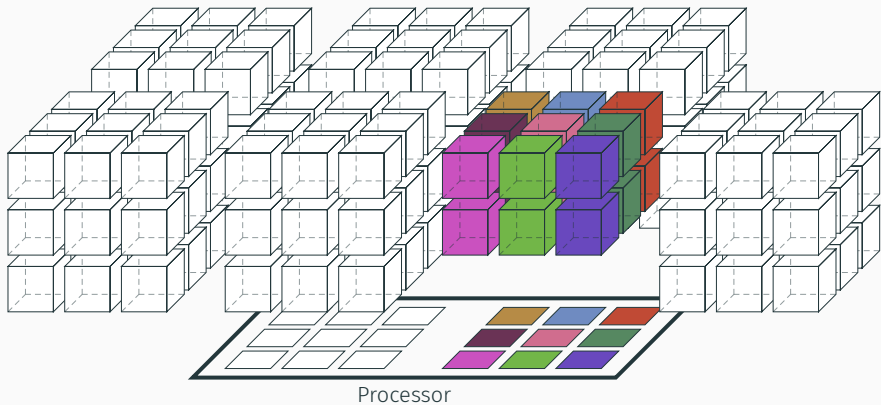
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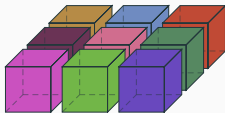
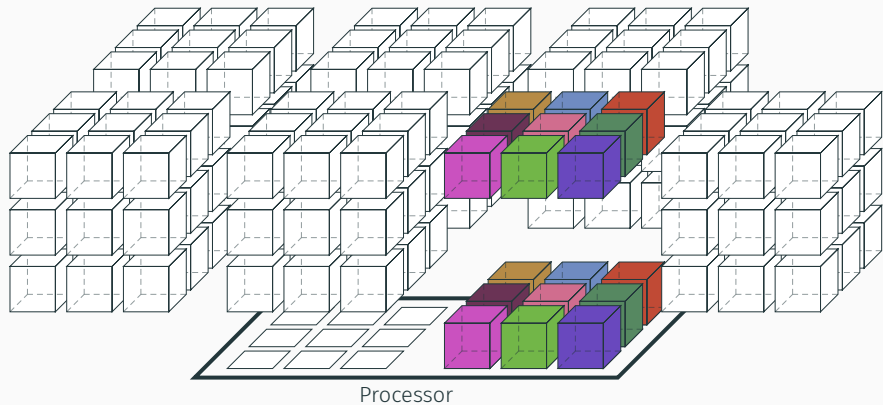
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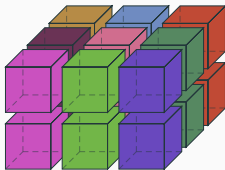
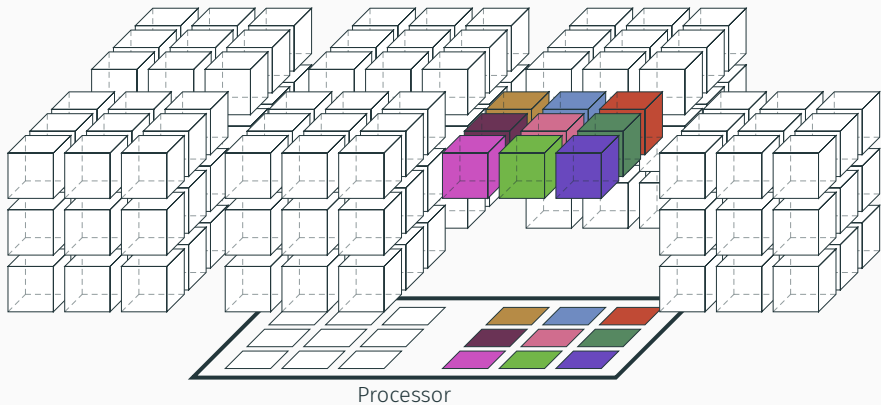
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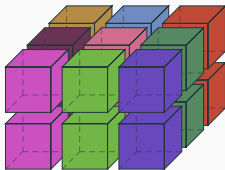
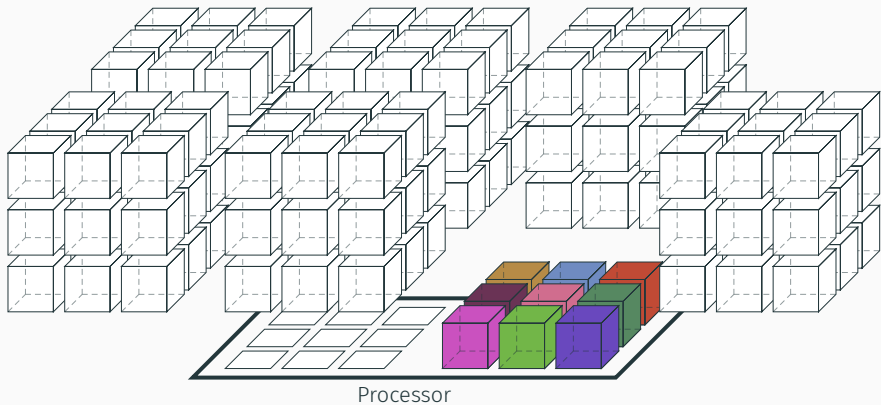
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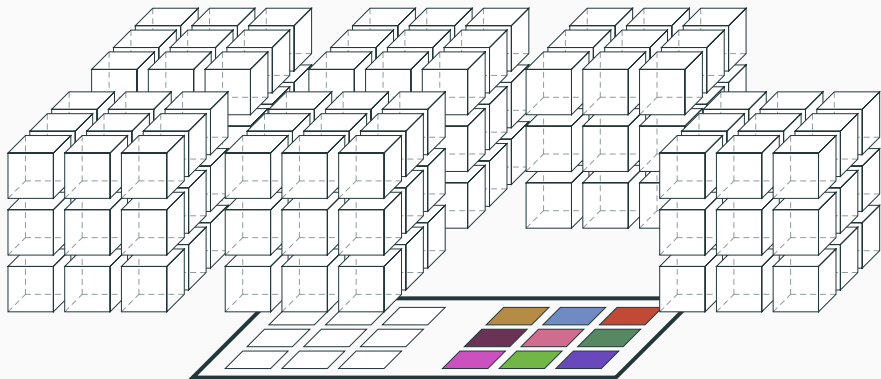


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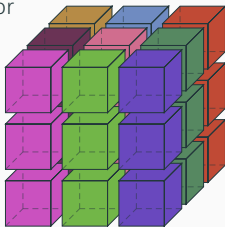




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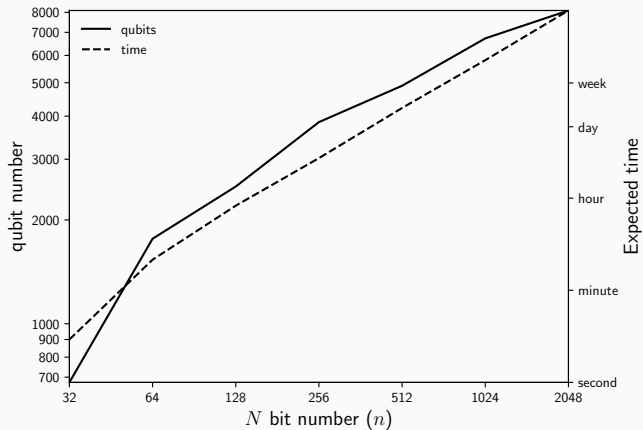
Processor



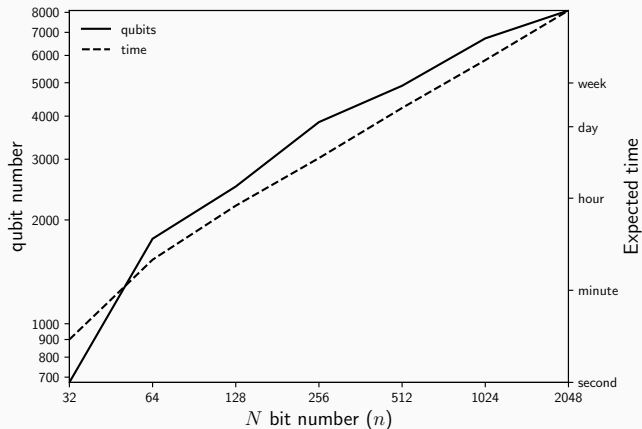
# Results

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# Resources required for factorization



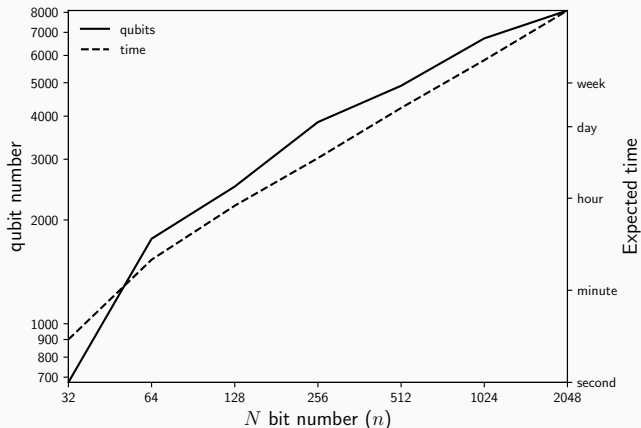
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## RSA 2 048 bits factorization

8 100 qubits, 175 days; maximum storage time:  $\approx$  2 h

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9 277 logical qubits for RSA-2 048, only slices of 2 logical qubits in the superconducting processor.

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### **Reduced decoherence in the memory**

Not really important because error-correction is exponentially efficient

# Implementation and perspectives

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- multimode cavity
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## Other problems

- optimization
- chemistry

The end

Thanks for your attention